

DIGITAL STORAGE OSCILLOSCOPE

Patent Number: JP4105070
Publication date: 1992-04-07
Inventor(s): IKEGAMI SHINICHI; others: 01
Applicant(s): KENWOOD CORP
Requested Patent: ☐ JP4105070
Application Number: JP19900222477 19900827
Priority Number(s):
IPC Classification: G01R13/20
EC Classification:
Equivalents: JP2070026C, JP7101222B

Abstract

PURPOSE: To enable an oscilloscope to display a pattern on a real time basis according to each eye by connecting a dual port RAM to address counters for specifying each address for write in most significant and less significant bits, and read from the less significant and most significant bits.

CONSTITUTION: A dual port RAM 2 capable of data read from and write in any address independently and asynchronously is used as a memory means. The aforesaid RAM 2 is connected to address counters and assigned with addresses for data write and read. More specifically, the first address counter for counting one pulse during a sampling period is used for data write in the most significant bit, while the second address counter for counting the pulse in a sampling pulse period is used for data write in the less significant bit. The third address counter for counting a pulse larger than a frequency fed from the second address counter is used for data read from the less significant bit, while the fourth address counter for counting overflow output from the third address counter is used for data read from the most significant bit. Furthermore, a pulse frequency from the third address counter is higher than a pulse frequency from the second address counter, and data stored in the RAM 2 is read out at a quick cycle. As a result, an enough after-image remains on a CRT screen, thereby enabling the virtual superposed display of eye patterns.

Data supplied from the esp@cenet database - 12